

PRODUCT : TFT TOUCH MODULE

MODULE NO. : WKS70WV024-WCT

SUPPLIER: WKS Technology Co.,LTD

DATE: JUN 15, 2019

SPECIFICATION

Revision: 0.0

WKS70WV024-WCT

This module uses ROHS material

This specification may change without prior notice in order to improve performance or quality. Please contact WKS R&D department for updated specification and product status before design for this product or release of this order.

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1、GENERAL INFORMATION

<i>Item of general information</i>	<i>Contents</i>		<i>Unit</i>
<i>LCD Display Size (Diagonal)</i>	7.0		<i>inch</i>
<i>Module Structure</i>	LCD Display + CTP Touch + PCB		-
<i>LCD Display Type</i>	TFT/TRANSMISSIVE		-
<i>LCD Display Mode</i>	Normally White		-
<i>Viewing Direction</i>	12 O'CLOCK		-
<i>Gray inversion Direction</i>	6 O'CLOCK		-
<i>Module size (W×H×T)</i>	185.00×105.00×8.45		<i>mm</i>
<i>Active area (W×H)</i>	154.21×85.92		<i>mm</i>
<i>Number of pixels (Resolution)</i>	800RGB×480		<i>pixel</i>
<i>Pixel pitch (W×H)</i>	0.1926 × 0.1790		<i>mm</i>
<i>LCD Driver IC</i>	-		-
<i>Module Interface Type</i>	<i>LCD</i>	<i>MCU 16bit/8bit interface</i>	-
	<i>CTP</i>	<i>IIC interface</i>	-
<i>Module Input voltage</i>	5.0V		<i>V</i>
<i>Module Power consumption</i>	-		<i>mW</i>
<i>Color Numbers</i>	16.7M		-
<i>Backlight Type</i>	White LED		-

3、ABSOLUTE MAXIMUM RATINGS

<i>Parameter of absolute maximum ratings</i>	<i>Symbol</i>	<i>Min</i>	<i>Max</i>	<i>Unit</i>
<i>Operating temperature</i>	<i>Top</i>	-20	70	℃
<i>Storage temperature</i>	<i>Tst</i>	-30	80	℃
<i>Humidity</i>	<i>RH</i>	-	90%(Max 60℃)	RH

Note: Absolute maximum ratings means the product can withstand short-term, not more than 120 hours. If the product is a long time to withstand these conditions, the life time would be shorter.

4、ELECTRICAL CHARACTERISTICS(DC CHARACTERISTICS)

<i>Parameter of DC characteristics</i>	<i>Symbol</i>	<i>Min.</i>	<i>Typ.</i>	<i>Max.</i>	<i>Unit</i>
<i>PCB operating voltage</i>	<i>VCC5V</i>	-	5.0	-	V
<i>LCD I/O operating voltage</i>	<i>VDD</i>	3.0	3.3	3.6	V
<i>Input voltage 'H' level</i>	<i>VIH</i>	2	-	3.6	V
<i>Input voltage 'L' level</i>	<i>VIL</i>	-0.3	-	0.8	V
<i>Output voltage 'H' level</i>	<i>VOH</i>	2.4	-	-	V
<i>Output voltage 'L' level</i>	<i>VOL</i>	-	-	0.4	V

5、BACKLIGHT CHARACTERISTICS

<i>Item of backlight characteristics</i>	<i>Symbol</i>	<i>Min.</i>	<i>Typ.</i>	<i>Max.</i>	<i>Unit</i>	<i>Remark</i>
<i>Forward Voltage</i>	<i>V_f</i>	8.4	9.3	9.9	<i>V</i>	<i>Note1</i>
<i>Forward Current</i>	<i>I_f</i>	-	140	-	<i>mA</i>	-
<i>Number of LED</i>	-	-	7*3	-	<i>Piece</i>	-
<i>LED Connection mode</i>	<i>P/S</i>	-	<i>Serial</i>	-	-	-
<i>Lifetime of LED</i>	-	-	10000	-	<i>hour</i>	<i>Note2</i>

Note:

- *Note1: The LED Supply Voltage is defined by the number of LED at Ta=25 °C and I_f=140mA.*
- *Note2: The LED lifetime define as the estimated time to 50% degradation of initial luminous. The LED lifetime could be decreased if operating I_f is larger than 140mA.*

6、ELECTRO-OPTICAL CHARACTERISTICS

Item of electro-optical characteristics		Symbol	Condition	Min.	Typ.	Max	Unit	Remark	Note
Response time		$T_r + T_f$	$\theta=0$ $\phi=0$ $T_a=25\text{ }^\circ\text{C}$	-	25	40	ms	FIG 1.	4
Contrast Ratio		CR		-	350	-	-	FIG 2.	1
Luminance uniformity		δWHITE		-	80	-	%	FIG 2.	3
Surface Luminance		L_v		-	300	-	cd/m ²	FIG 2.	2
CIE (x, y) chromaticity	White	White x	$\theta=0$ $\phi=0$ $T_a=25\text{ }^\circ\text{C}$	-	0.310	-	-	FIG 2.	5
		White y		-	0.33	-			
	Red	Red x		-	0.587	-			
		Red y		-	0.331	-			
	Green	Green x		-	0.344	-			
		Green y		-	0.571	-			
	Blue	Blue x		-	0.146	-			
		Blue y		-	0.0092	-			
Viewing angle range	$\phi=90(12\text{ o'clock})$		CR ≥ 10	-	50	-	deg	FIG 3.	6
	$\phi=270(6\text{ o'clock})$			-	70	-	deg		
	$\phi=0(3\text{ o'clock})$			-	70	-	deg		
	$\phi=180(9\text{ o'clock})$			-	70	-	deg		
NTSC ratio		-	-	-	50	-	%	-	-

Note 1. Contrast Ratio (CR) is defined mathematically by the following formula. For more information see FIG 2.:

$$\text{Contrast Ratio(CR)} = \frac{\text{Average Surface Luminance with all white pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}}{\text{Average Surface Luminance with all black pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

L_v =Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5,P6,P7,P8,P9)

Note 3. The uniformity in surface luminance (δWHITE) is determined by measuring

luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}}$$

Note 4. Response time is the time required for the display to transition from White to black (Rise Time, T_r) and from black to white (Decay Time, T_f). For additional information see FIG 1.

Note 5. CIE (x, y) chromaticity, The x, y value is determined by screen active area position 5. For more information see FIG 2.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of contrast ratio is 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note 7. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

Note 8. For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time

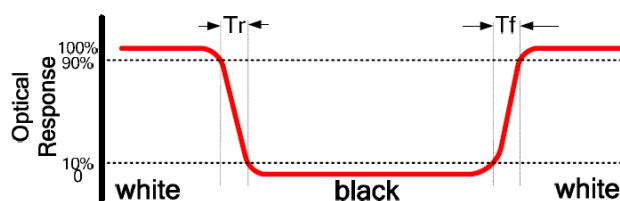


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity,

CIE (x , y) chromaticity

A : H/6 ;

B : V/6 ;

H,V : Active Area(AA) size

Measurement instrument: BM-7; Light spot size=5mm, 350mm distance from the LCD surface to detector lens.

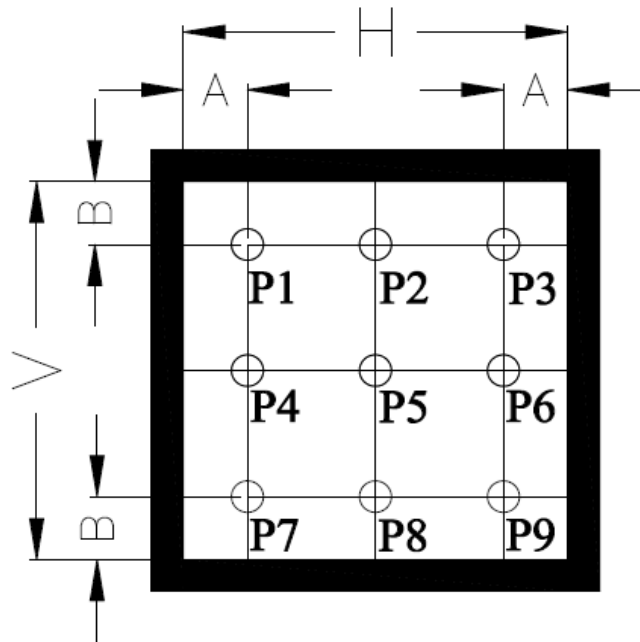
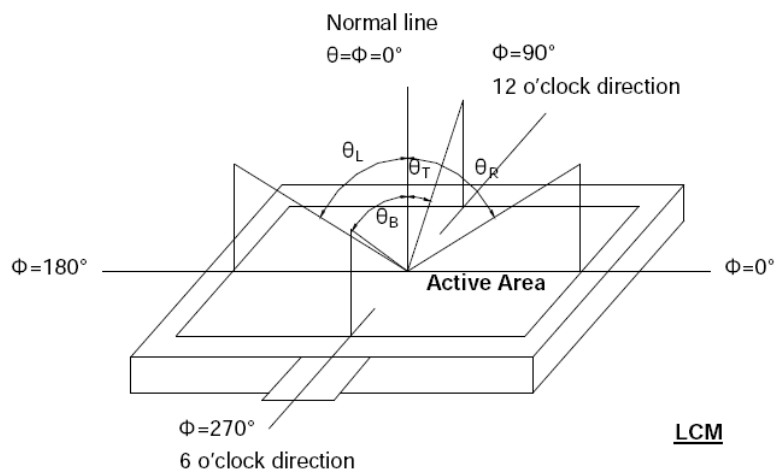


FIG.3. The definition of viewing angle



7、INTERFACE DESCRIPTION

7.1、LCD1 Interface Description

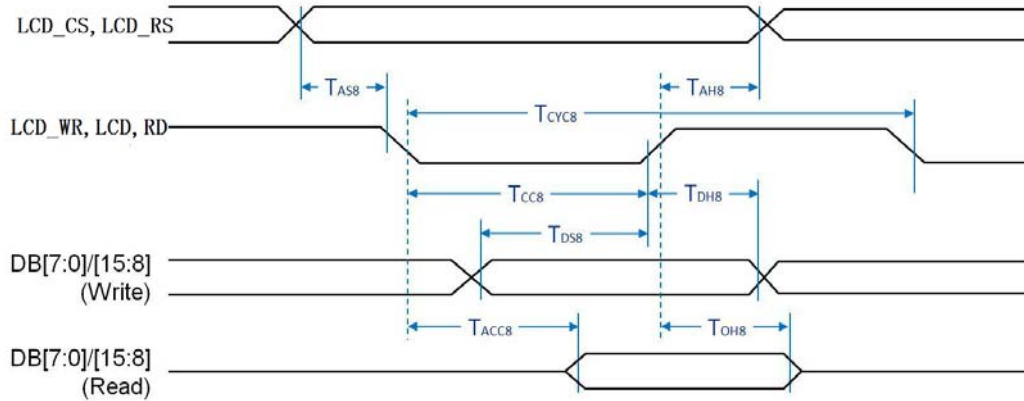
NO.	Symbol	I/O	DESCRIPTION
1	CS	I	Chip select
2	RS	I	Data/Command select
3	WR	I	Write strobe signal
4	RD	I	Read strobe signal
5	LCD_RST	I	LCD RESET signal, Low is active
6~21	DB0~ DB15	I/O	Data bus(D0:LSB; D15:MSB)
22	GND	Power Supply	Power ground
23	BL_CTRL	I	Backlight control pin
24~25	OPEN	-	No connection
26~27	GND	Power supply	Power ground
28	VDD5V	Power supply	Module Power input (5V Typ.) (*note1)
29	NC	O	No connection
30	CTP_SDA	I	TP Serial Data Input
31	CTP_INT	I	TP INT Interrupt Output
32	NC	-	No connection
33	CTP_CS	I	TP Chip reset pin
34	CTP_SCL	I	TP Clock Input

7.2、LCD2 Interface Description

NO.	Symbol	I/O	DESCRIPTION
1~2	GND	Power supply	Power ground
3	CS	I	Chip select
4	RS	I	Data/Command select
5	WR	I	Write strobe signal
6	RD	I	Read strobe signal
7	LCD_RST	I	LCD RESET signal, Low is active
8~23	DB0~ DB15	I/O	Data bus(D0:LSB; D15:MSB)
24	GND	Power supply	Power ground
25	BL_CTRL	I	Backlight control pin
26~28	NC	-	No connection
29~30	VDD5V	Power supply	Module Power input (5V Typ.)
31	NC	O	No connection
32	CTP_SDA	I	TP Serial Data Input
33	CTP_INT	I	TP INT Interrupt Output
34	NC	-	No connection
35	CTP_RST	I	TP Chip Select Input
36	CTP_CLK	I	TP Clock Input
37	NC	I	No connection
38	NC	I	No connection
39~40	GND	-	Power ground

8、 INPUT TIMING

8.1、 8080 Parallel Mode Interface Timing



Symbol	Parameter	Rating		Unit	Note
		Min.	Max.		
T_{CYC8}	Cycle Time	50	--	ns	tc is one system clock period: tc = 1/SYS_CLK
T_{CC8}	Strobe Pulse Width	20	--	ns	
T_{AS8}	Address Setup Time	0	--	ns	
T_{AH8}	Address Hold Time	10	--	ns	
T_{DS8}	Data Setup Time	20	--	ns	
T_{DH8}	Data Hold Time	10	--	ns	
T_{ACC8}	Data Output Access Time	0	20	ns	
T_{OH8}	Data Output Hold Time	0	20	ns	

Register Write:

1. Address Write: Write the Register's Address. For example, 00h i.e. REG[00h], 01h i.e. REG[01h], 02h i.e. REG[02h]
2. Data Write: Write Data to the Register

Register Read:

1. Address Write: Write the Register's Address
2. Data Write: Read Data from the Register

Displays Memory (Display RAM) is where the TFT screen image data is stored,. Host through interface and write data into Display RAM. The procedure of access Display RAM is as following:

Display RAM Write:

1. Set the Active Window Registers before writing any image data.
2. Perform an register write to Graphic R/W Position Register 0, REG[5Fh]).
3. Repeat step 2 until setup all the Active Window & Graphic R/W Position Coordinates.
4. Perform an address write to point to Memory Data Port Register (REG[04h])
5. Perform data writes to fill the window. Each write to the Memory Data Port will auto-increment the internal memory address.

9、RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition
1	High Temperature Storage	80 °C/120 hours
2	Low Temperature Storage	-30 °C/120 hours
3	High Temperature Operating	70 °C/120 hours
4	Low Temperature Operating	-20 °C/120 hours
5	Temperature Cycle Storage	-20 °C(30min.)~25(5min.)~70 °C(30min.)×10cycles

A、Inspection after test:

Inspection after 2~4 hours storage at room temperature, the sample shall be free from defects:

- Air bubble in the LCD;
- Sealleak;
- Non-display;
- Missing segments;
- Glass crack;
- Current is twice higher than initial value.

B、Remark:

- The test samples should be applied to only one test item.
- Sample size for each test item is 5~10pcs.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

10、INSPECTION CRITERION

This specification is made to be used as the standard of acceptance/rejection criteria for TFT-LCD/IPS TFT-LCD module product, and this specification is applicable only in the case that the size of module equal to or exceed than 3.5 inch.

10.1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993,normal level 2 and based on:

Major defect: AQL 0.65

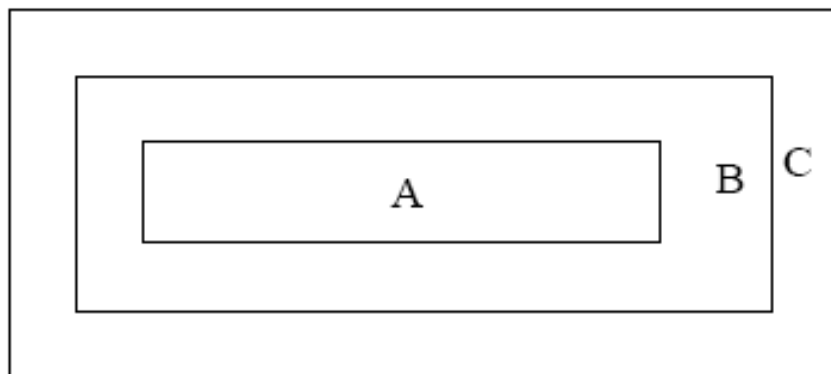
Minor defect: AQL 1.5

10.2 Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature 20~25 °C and normal humidity 60 ±15%RH)

10.3 Definition of Inspection Item.

A、Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

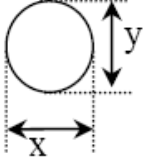
B、 Definition of some visual defect

<i>Bright dot</i>	<i>Because of losing all or part function, bad pixel dots appear bright and the size is more than 50% of one dot in which LCD panel is displaying under black pattern.</i>
<i>Dark dot</i>	<i>Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture, or pure whiter picture.</i>

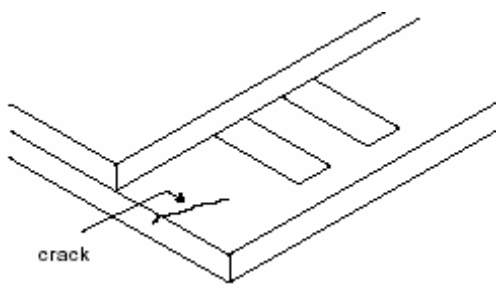
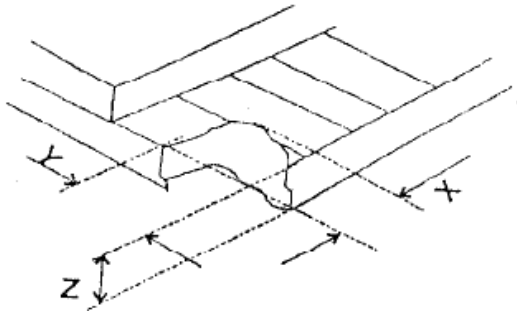
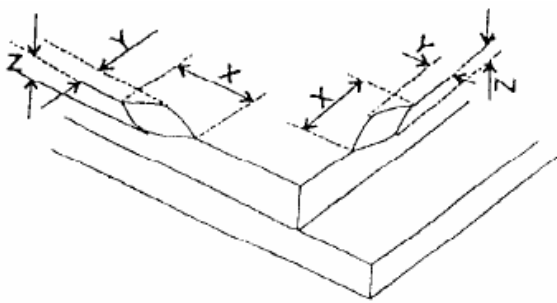
10.4 Major Defect

<i>Item No.</i>	<i>Items to be inspected</i>	<i>Inspection standard</i>	<i>Classification of defects</i>
<i>1</i>	<i>Functional defects</i>	<i>1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Excess power consumption 6) Backlight no lighting, flickering and abnormal lighting</i>	<i>major</i>
<i>2</i>	<i>Missing</i>	<i>Missing component</i>	
<i>3</i>	<i>Outline dimension</i>	<i>Overall outline dimension beyond the drawing is not allowed</i>	

10.5 Minor Defect

Item No.	Items to be inspected	Inspection standard					Classification of defects	
1	Bright dot /dark dot defect	Zone		Acceptable Qty			Minor	
				A+B				C
				3.5" ~ 7"	7~10.1"	>10.1"		
		Bright pixel dot		1	2	3		Acceptable
		Dark pixel dot		4	4	4		
		2bright dots adjacent		0	0	0		
		2dark dots adjacent		0	0	0		
Total bright and dark dots		5	6	7				
<p>Note: Minimum distance between defective dots is more than 5mm; Pixel dots' function is normal, but bright dots caused by foreign material and other reasons are judged by the dot defect of 5.2.</p>								
2	<p>Dot defect</p>  <p>$\Phi = (x+y) / 2$</p>	Zone		Acceptable Qty			Minor	
				A+B				C
		Size(mm)		3.5"~7"	7~10.1"	>10.1"		
		$\Phi \leq 0.2$		Acceptable	Acceptable	Acceptable		Acceptable
		$0.2 < \Phi \leq 0.5$		4	5	6		
$\Phi > 0.5$		0	0	0				
<p>Note:</p> <ol style="list-style-type: none"> 1. Minimum distance between defective dots is more than 5 mm; 2. The quantity of defect is zero in operating condition. 								
3	Linear defect	Zone		Acceptable Qty			Minor	
				A+B				C
		Size (mm)		3.5"~7"	7~10.1"	>10.1"		
		Length	Width	3.5"~7"	7~10.1"	>10.1"		Acceptable
		Ignore	$W \leq 0.05$	Acceptable	Acceptable	Acceptable		
$L \leq 5.0$	$0.05 < W \leq 0.1$	4	5	6	Acceptable			
$L > 5.0$	$W > 0.1$	0	0	0				

4	Polarizer defect	<p>5.4.1 Polarizer Position</p> <p>(i) Shifting in position should not exceed the glass outline dimension.</p> <p>(ii) Incomplete covering of the viewing area due to shifting is not allowed.</p> <p>5.4.2 Dirt on polarizer</p> <p>Dirt which can be wiped easily should be acceptable.</p> <p>5.4.3 Polarizer Dent & Air bubble</p> <table border="1"> <thead> <tr> <th colspan="2" rowspan="2">Zone</th> <th colspan="3">Acceptable Qty</th> <th rowspan="2">C</th> </tr> <tr> <th colspan="3">A+B</th> </tr> <tr> <th rowspan="2">Size(mm)</th> <th rowspan="2"></th> <th>3.5"~7"</th> <th>7~10.1"</th> <th>>10.1"</th> <th rowspan="2">Acceptable</th> </tr> </thead> <tbody> <tr> <td>$\Phi \leq 0.2$</td> <td></td> <td>Acceptable</td> <td>Acceptable</td> <td>Acceptable</td> </tr> <tr> <td>$0.2 < \Phi \leq 0.5$</td> <td></td> <td>4</td> <td>5</td> <td>6</td> </tr> <tr> <td>$\Phi > 0.5$</td> <td></td> <td>0</td> <td>0</td> <td>0</td> </tr> </tbody> </table> <p>5.4.4 Polarizer scratch</p> <p>(i) If the polarizer scratch can be seen after cover assembling or in the operating condition, judge by the linear defect of 5.3.</p> <p>(ii) If the polarizer scratch can be seen only in non-operating condition or some special angle, judge by the following:</p> <table border="1"> <thead> <tr> <th colspan="2" rowspan="2">Zone</th> <th colspan="3">Acceptable Qty</th> <th rowspan="2">C</th> </tr> <tr> <th colspan="3">A+B</th> </tr> <tr> <th rowspan="2">Length</th> <th rowspan="2">Width</th> <th rowspan="2">Size (mm)</th> <th>3.5"~7"</th> <th>7~10.1"</th> <th>>10.1"</th> <th rowspan="2">Acceptable</th> </tr> </thead> <tbody> <tr> <td>Ignore</td> <td>$W \leq 0.05$</td> <td></td> <td>Acceptable</td> <td>Acceptable</td> <td>Acceptable</td> </tr> <tr> <td>$1.0 < L \leq 5.0$</td> <td>$0.05 < W \leq 0.20$</td> <td></td> <td>4</td> <td>5</td> <td>6</td> </tr> <tr> <td>$L > 5.0$</td> <td>$W > 0.2$</td> <td></td> <td>0</td> <td>0</td> <td>0</td> </tr> </tbody> </table>	Zone		Acceptable Qty			C	A+B			Size(mm)		3.5"~7"	7~10.1"	>10.1"	Acceptable	$\Phi \leq 0.2$		Acceptable	Acceptable	Acceptable	$0.2 < \Phi \leq 0.5$		4	5	6	$\Phi > 0.5$		0	0	0	Zone		Acceptable Qty			C	A+B			Length	Width	Size (mm)	3.5"~7"	7~10.1"	>10.1"	Acceptable	Ignore	$W \leq 0.05$		Acceptable	Acceptable	Acceptable	$1.0 < L \leq 5.0$	$0.05 < W \leq 0.20$		4	5	6	$L > 5.0$	$W > 0.2$		0	0	0	Minor
		Zone			Acceptable Qty				C																																																										
A+B																																																																			
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$1.0 < L \leq 5.0$	$0.05 < W \leq 0.20$		4	5	6																																																														
$L > 5.0$	$W > 0.2$		0	0	0																																																														
5	MURA	Using 3% ND filter, it's NG if it can be seen in R,G,B picture.	Minor																																																																
	White/Black dot (MURA)	Visible under: ND3%; $D \leq 0.15mm$, Acceptable; $0.15mm < D \leq 0.5mm$, $N \leq 4$; $D > 0.5mm$, Not allowable.																																																																	

6	Glass defect	<p>(i) Crack Cracks are not allowed.</p> 	Minor								
		<p>(ii) TFT chips on corner</p>  <table border="1" style="width: 100%; text-align: center;"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> <th>Acceptable</th> </tr> </thead> <tbody> <tr> <td>≤ 3.0</td> <td>≤ 3.0</td> <td>Not more than the thickness of glass</td> <td>$N \leq 3$</td> </tr> </tbody> </table> <p>Chips on the corner of terminal shall not be allowed to extend into the ITO pad or expose perimeter seal.</p>	X	Y	Z	Acceptable	≤ 3.0	≤ 3.0	Not more than the thickness of glass	$N \leq 3$	Minor
		X	Y	Z	Acceptable						
≤ 3.0	≤ 3.0	Not more than the thickness of glass	$N \leq 3$								
<p>(iii) Usual surface crack</p>  <table border="1" style="width: 100%; text-align: center;"> <thead> <tr> <th>X</th> <th>Y</th> <th>Z</th> <th>Acceptable</th> </tr> </thead> <tbody> <tr> <td>≤ 1.5</td> <td>≤ 1.5</td> <td>Not more than the thickness of glass</td> <td>$N \leq 4$</td> </tr> </tbody> </table> <p>It is only applicable to the upper glass of LCD.</p>	X	Y	Z	Acceptable	≤ 1.5	≤ 1.5	Not more than the thickness of glass	$N \leq 4$	Minor		
X	Y	Z	Acceptable								
≤ 1.5	≤ 1.5	Not more than the thickness of glass	$N \leq 4$								

10.6 Module Cosmetic Criteria

Item No.	Items to be inspected	Inspection Standard	Classification of defects
1	Difference in Spec.	Not allowable	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
3	Soldering defects	No soldering missing	Major
		No soldering bridge	Major
		No cold soldering	Minor
4	Resist flaw on PCB	Visible copper foil ($\Phi 0.5$ mm or more) on substrate pattern is not allowed	Minor
5	FPC gold finger	No dirt, breaking, oxidation lead to black	Major
6	Backlight plastic frame	No deformation, crack, breaking, backlight positioning column breaking, obvious nick.	Minor
7	Marking printing effect	No dark marking, incomplete, deformation lead to unable to judge	Minor
8	Accretion of metallic Foreign matter	No accretion of metallic foreign matter (Not exceed $\Phi 0.2$ mm)	Minor
9	Stain	No stain to spoil cosmetic badly	Minor
10	Plate discoloring	No plate fading, rusting and discoloring	Minor
11	1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.	Minor
		b. Components side(In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor
	2. Flat packages	Either 'Toe'(A) or 'Seal'(B)of the lead to be covered by "Filet". Lead form to be assume over Solder.	Minor
	3. Chips	$(3/2) H \geq h \geq (1/2) H$	Minor
4. Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \geq 0.13$ mm. The diameter of solder ball $d \leq 0.15$ mm.	Minor	
	b. The quantity of solder balls or solder splashes isn't beyond 5 in 600 mm ² .	Minor	
	c. Solder balls/Solder splashes do not violate minimum electrical clearance.	Major	

