

PRODUCT : **TFT TOUCH MODULE**

MODULE NO. : **WKS43WV002-WCT**

SUPPLIER: **WKS Technology Co.,LTD**

DATE: **Mar 10, 2018**

SPECIFICATION

Revision: 0.0

WKS43WV002-WCT

This module uses ROHS material

This specification may change without prior notice in order to improve performance or quality. Please contact WKS R&D department for updated specification and product status before design for this product or release of this order.

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REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
0.0	2018-03-10	First release	Preliminary

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1、GENERAL INFORMATION

Item of general information	Contents		Unit
LCD Display Size(Diagonal)	4.3		inch
Module Structure	LCD Display + CTP Touch + PCB		-
LCD Display Type	TFT/TRANSMISSIVE		-
LCD Display Mode	Normally White		-
Recommended Viewing Direction	6		o'clock
Gray inversion Direction	12		o'clock
Module size (W×H×T)	62.26×117.50×6.20		mm
Active area (W×H)	56.16×93.60		mm
Number of pixels(Resolution)	480RGB×800		pixel
Pixel pitch (W×H)	0.117×0.117		mm
Color Pixel Arrangement	RGB Stripe		-
LCD Driver IC	-		-
Interface Type	LCD	16-bit 8080 interface	-
	CTP	I2C interface	-
Input voltage	5V&3.3V		V
Power consumption	-		mW
Color Numbers	65K		-
Backlight Type	White LED		-

3、ABSOLUTE MAXIMUM RATINGS

Parameter of absolute maximum ratings	Symbol	Min	Max	Unit
Operating temperature	Top	-20	70	°C
Storage temperature	Tst	-30	80	°C
Humidity	RH	-	90%(Max 60°C)	RH

Note: Absolute maximum ratings means the product can withstand short-term, not more than 120 hours. If the product is a long time to withstand these conditions, the life time would be shorter.

4、ELECTRICAL CHARACTERISTICS(DC CHARACTERISTICS)

Parameter of DC characteristics	Symbol	Min.	Typ.	Max.	Unit
Module Operating voltage	VCC3.3V	-	3.3	-	V
Module Operating voltage	VCC5V	-	5.0	-	
Input voltage 'H' level	VIH	0.7*VCC3.3	-	VCC3.3	V
Input voltage 'L' level	VIL	VSS	-	0.3*VCC3.3	V
Output voltage 'H' level	VOH	0.8* VCC3.3	-	VCC3.3	V
Output voltage 'L' level	VOL	VSS	-	0.2*VCC3.3	V

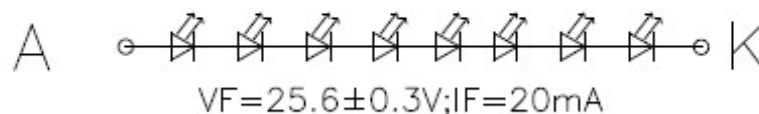
5、BACKLIGHT CHARACTERISTICS

Item of backlight characteristics	Symbol	Min.	Typ.	Max.	Unit	Remark
Forward Voltage	Vf	25.2	25.6	25.9	V	Note1
Forward Current	If	-	20	-	mA	-
Number of LED	-	-	8	-	Piece	-
LED Connection mode	P/S	-	Serial	-	-	-
Lifetime of LED	-	-	10000	-	hour	Note2

Note:

- Note1: The LED Supply Voltage is defined by the number of LED at Ta=25°C and If=20mA.
- Note2: The LED lifetime define as the estimated time to 50% degradation of initial luminous. The LED lifetime could be decreased if operating If is lager than 20mA.
- Backlight circuit:

Backlight Circuit:



6、CTP CHARACTERISTICS

Item of CTP characteristics	Specification	Unit	Remark
Panel Type	Glass Cover + Glass Sensor	-	-
Resolution	480 × 800	pixel	-
Surface Hardness	6H	-	-
Transparency	≥82%	-	-
Driver IC	-	-	-
Interface Type	I2C	-	-
Support Points	5	-	-
Sampling Rate	20~100	Hz	-
Supply voltage	3.3	V	-

7、ELECTRO-OPTICAL CHARACTERISTICS

Item of electro-optical characteristics		Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	Note
Response time		Tr+Tf	$\theta=0$ $\phi=0$ Ta=25°C	-	20	-	ms	FIG 1.	4
Contrast Ratio		CR		-	350	-	-	FIG 2.	1
Luminance uniformity		δ WHITE		-	80	-	%	FIG 2.	3
Surface Luminance		Lv		-	200	-	cd/m2	FIG 2.	2
CIE (x, y) chromaticity	White	White x	$\theta=0$ $\phi=0$ Ta=25°C	-	0.317	-	-	FIG 2.	5
		White y		-	0.324	-			
	Red	Red x		-	0.633	-			
		Red y		-	0.341	-			
	Green	Green x		-	0.324	-			
		Green y		-	0.551	-			
	Blue	Blue x		-	0.153	-			
		Blue y		-	0.143	-			
Viewing angle range	$\phi=90(12\text{ o'clock})$		CR \geq 10	-	50	-	deg	FIG 3.	6
	$\phi=270(6\text{ o'clock})$			-	60	-	deg		
	$\phi=0(3\text{ o'clock})$			-	65	-	deg		
	$\phi=180(9\text{ o'clock})$			-	65	-	deg		
NTSC ratio		-	-	-	50	-	%	-	-

Note 1. Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.:

$$\text{Contrast Ratio(CR)} = \frac{\text{Average Surface Luminance with all white pixels(P1,P2, P3,P4, P5,P6,P7,P8,P9)}}{\text{Average Surface Luminance with all black pixels(P1,P2, P3,P4, P5,P6,P7,P8,P9)}}$$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv=Average Surface Luminance with all white pixels (P1,P2,P 3,P4, P5,P6,P7,P8,P9)

Note 3. The uniformity in surface luminance (δWHITE) is determined by measuring

luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see FIG 2.

$$\delta_{\text{WHITE}} = \frac{\text{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}}{\text{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}}$$

Note 4. Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1.

Note 5. CIE (x, y) chromaticity ,The x,y value is determined by screen active area position 5. For more information see FIG 2.

Note 6. Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of contrast ratio is 10.The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note 7. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

Note 8. For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time

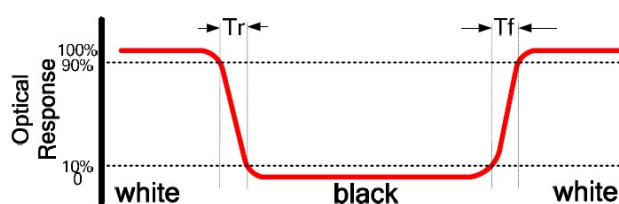


FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity, CIE (x , y) chromaticity

$A : H/6 ;$

$B : V/6 ;$

H, V : Active Area(AA) size

Measurement instrument: BM-7; Light spot size=5mm, 350mm distance from the LCD surface to detector lens.

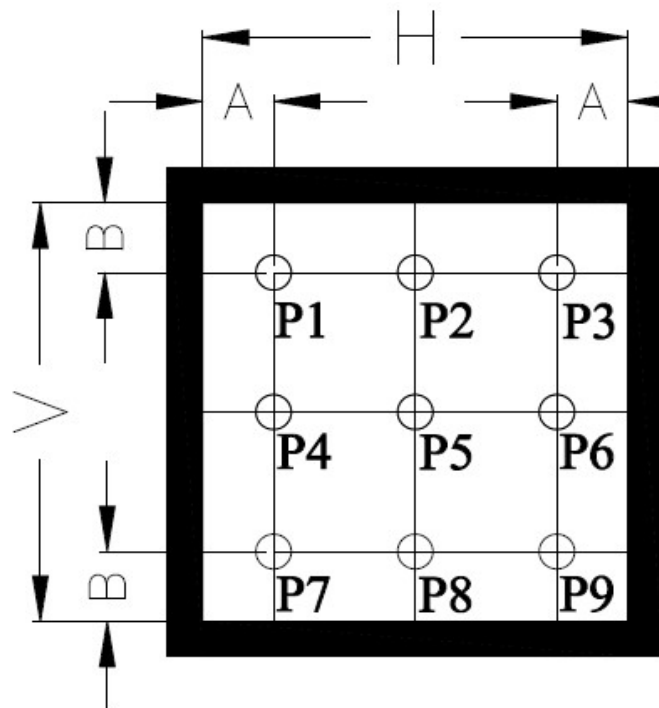
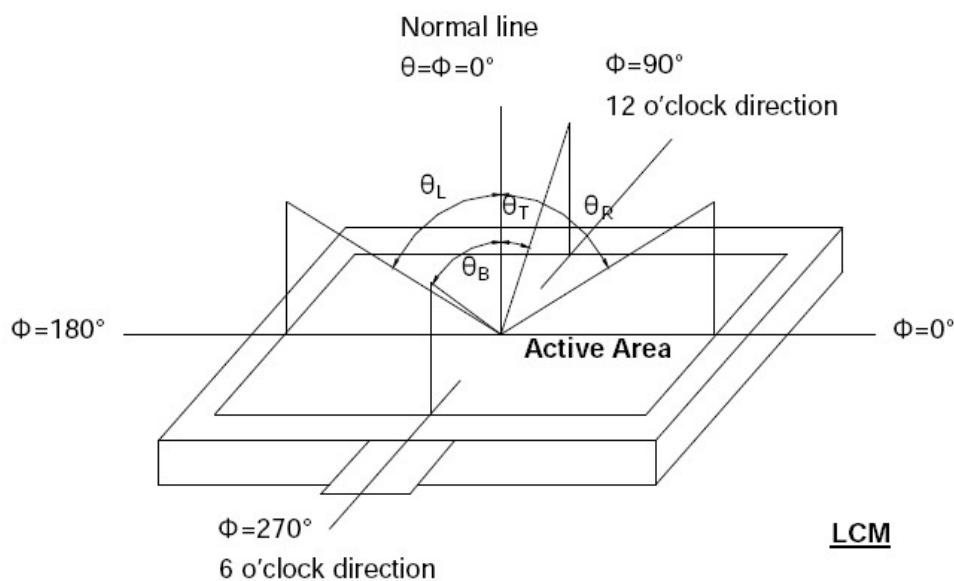


FIG.3. The definition of viewing angle



8、INTERFACE DESCRIPTION

A、Input Interface Description(CON1)

NO.	Symbol	I/O	DESCRIPTION
1	CS	I	Chip select
2	RS	I	Data/Command select
3	WR	I	Write strobe signal
4	RD	I	Read strobe signal
5	RESET	I	LCD RESET signal, Low is active
6~21	D0~D15	I/O	Data bus(D0:LSB; D15:MSB)
22	GND	Power supply	Power ground
23	BL_CTRL	I	Backlight control pin
24~25	VCC_3.3V	Power supply	Module Power input (3.3V Typ.)
26~27	GND	Power supply	Power ground
28	VCC_5V	-	Module Power input (5.0V Typ.)
29	NC	-	No connection
30	CTP_SDA	I	CTP I2C data input and output
31	CTP_INT	O	CTP External interrupt to the host
32	NC	-	No connection
33	CTP_RST	I	CTP external reset signal, Low is active
34	CTP_SCL	I	CTP I2C clock input

B、Input Interface Description(J1)

NO.	Symbol	I/O	DESCRIPTION
1~2	NC	-	No connection
3	CS	I	Chip select
4	RS	I	Data/Command select
5	WR	I	Write strobe signal
6	RD	I	Read strobe signal
7	RESET	I	LCD RESET signal, Low is active
8~23	D0~D15	I/O	Data bus(D0:LSB; D15:MSB)
24	GND	Power supply	Power ground
25	BL_CTRL	I	Backlight control pin
26~27	VCC_3.3V	Power supply	Module Power input (3.3V Typ.)
28~29	GND	Power supply	Power ground
30	VCC_5V	-	Module Power input (5.0V Typ.)
31	NC	-	No connection
32	CTP_SDA	I	CTP I2C data input and output
33	CTP_INT	O	CTP External interrupt to the host
34	NC	-	No connection
35	CTP_RST	I	CTP external reset signal, Low is active
36	CTP_SCL	I	CTP I2C clock input
37~40	NC	-	No connection

9、INPUT TIMING

Parallel Interface Input Timing Table (80-Series MCU)

Signal	Symbol	Parameter	MIN	MAX	Unit	Description
WR	t_{WC}	Write cycle	33	-	ns	
	t_{WRH}	Control pulse "H" duration	15	-	ns	
	t_{WRL}	Control pulse "L" duration	15	-	ns	
RD(ID)	t_{RC}	Read cycle(ID)	160	-	ns	When read ID data
	t_{RDH}	Control pulse "H" duration(ID)	90	-	ns	
	t_{RDL}	Control pulse "L" duration(ID)	45	-	ns	
RD(FM)	t_{RCFM}	Read cycle(FM)	400	-	ns	When read from frame memory
	t_{RDHFM}	Control pulse "H" duration(FM)	250	-	ns	
	t_{RDLFM}	Control pulse "L" duration(FM)	150	-	ns	
RS	t_{AST}	Address setup time(Write)	0	-	ns	
		Address setup time(Read)	10	-	ns	
	t_{AHT}	Address hold time	2	-	ns	
D[15:0]	t_{DST}	Data setup time	15	-	ns	
	t_{DHT}	Data hold time	10	-	ns	
	t_{RAT}	Read access time(ID)	-	40	ns	
	t_{RATFM}	Read access time(FM)	-	150	ns	
	t_{ODH}	Output disable time	5	-	ns	

10、RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition
1	High Temperature Storage	80°C/120 hours
2	Low Temperature Storage	-30°C/120 hours
3	High Temperature Operating	70°C/120 hours
4	Low Temperature Operating	-20°C/120 hours
5	Temperature Cycle Storage	-20°C(30min.)~25(5min.)~70°C(30min.)×10cycles

A、Inspection after test:

Inspection after 2~4 hours storage at room temperature, the sample shall be free from defects:

- Air bubble in the LCD;
- Sealleak;
- Non-display;
- Missing segments;
- Glass crack;
- Current is twice higher than initial value.

B、Remark:

- The test samples should be applied to only one test item.
- Sample size for each test item is 5~10pcs.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

11、INSPECTION CRITERION

This specification is made to be used as the standard of acceptance/rejection criteria for TFT-LCD/IPS TFT-LCD module product, and this specification is applicable only in the case that the size of module equal to or exceed than 4.3 inch.

11.1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993,normal level 2 and based on:

Major defect: AQL 0.65

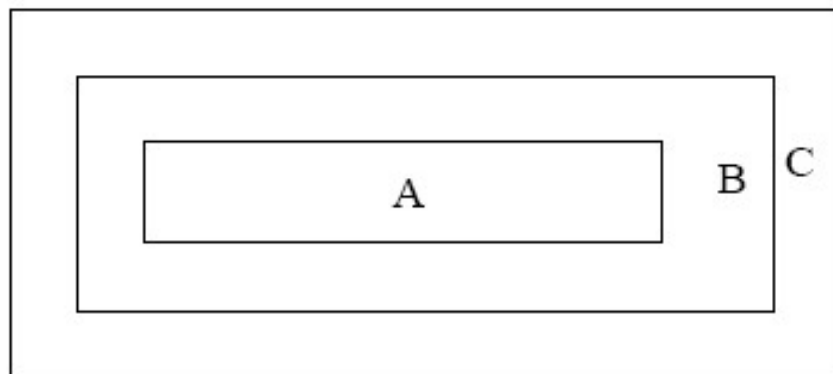
Minor defect: AQL 1.5

11.2 Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45° against perpendicular line. (Normal temperature 20~25℃ and normal humidity 60 ±15%RH)

11.3 Definition of Inspection Item.

A、 Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

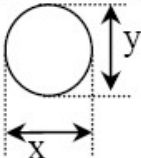
B、 Definition of some visual defect

Bright dot	Because of losing all or part function, bad pixel dots appear bright and the size is more than 50% of one dot in which LCD panel is displaying under black pattern.
Dark dot	Dots appear dark and unchanged in size in which LCD panel is displaying under pure red, green, blue picture, or pure whiter picture.

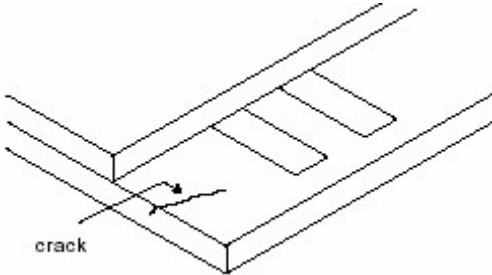
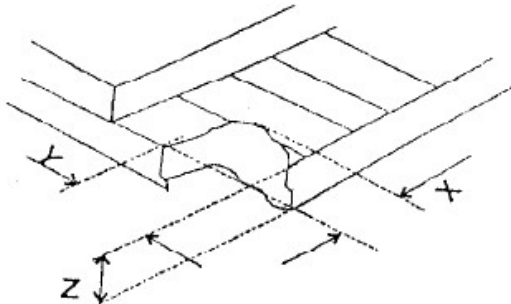
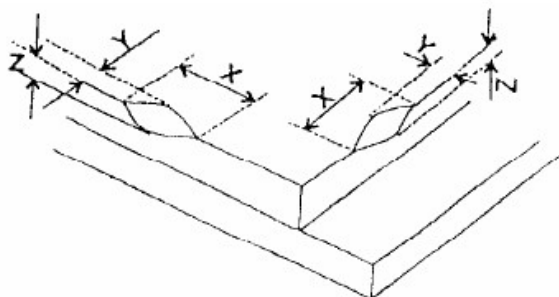
11.4 Major Defect

Item No.	Items to be inspected	Inspection standard	Classification of defects
1	Functional defects	1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Excess power consumption 6) Backlight no lighting, flickering and abnormal lighting	major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	


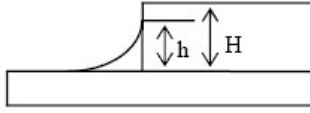
5、Minor Defect

Item No.	Items to be inspected	Inspection standard					Classification of defects				
1	Bright dot /dark dot defect	Zone		Acceptable Qty			Acceptable	Minor			
				A+B							
				4.3''~7''	7~10.1''	>10.1''					
		Bright pixel dot		1	2	3					
		Dark pixel dot		4	4	4					
		2bright dots adjacent		0	0	0					
		2dark dots adjacent		0	0	0					
		Total bright and dark dots		5	6	7					
Note: Minimum distance between defective dots is more than 5mm; Pixel dots' function is normal, but bright dots caused by foreign material and other reasons are judged by the dot defect of 5.2.											
2	<div>Dot defect</div> <div></div> <div>$\Phi=(x+y) / 2$</div>	Zone		Acceptable Qty			Acceptable	Minor			
				A+B							
				4.3''~7''	7~10.1''	>10.1''					
		Size(mm) $\Phi \leq 0.2$		Acceptable	Acceptable	Acceptable					
		$0.2 < \Phi \leq 0.5$		4	5	6					
		$\Phi > 0.5$		0	0	0					
		Note: 1. Minimum distance between defective dots is more than 5 mm; 2. The quantity of defect is zero in operating condition.									
		3	Linear defect	Zone		Acceptable Qty			Acceptable	Minor	
A+B											
Size (mm)											
Length	Width			4.3''~7''	7~10.1''	>10.1''					
Ignore	$W \leq 0.05$			Acceptable	Acceptable	Acceptable					
$L \leq 5.0$	$0.05 < W \leq 0.1$			4	5	6					
$L > 5.0$	$W > 0.1$			0	0	0					

4	Polarizer defect	<p>5.4.1 Polarizer Position</p> <p>(i) Shifting in position should not exceed the glass outline dimension.</p> <p>(ii) Incomplete covering of the viewing area due to shifting is not allowed.</p> <p>5.4.2 Dirt on polarizer</p> <p>Dirt which can be wiped easily should be acceptable.</p> <p>5.4.3 Polarizer Dent & Air bubble</p> <table><tr><th colspan="2" rowspan="2">Zone Size(mm)</th><th colspan="3">Acceptable Qty</th><th rowspan="2">C</th></tr><tr><th colspan="3">A+B</th></tr><tr><th colspan="2"></th><th>4.3''~7''</th><th>7~10.1''</th><th>>10.1''</th><th rowspan="3">Acceptable</th></tr><tr><td colspan="2">$\Phi \leq 0.2$</td><td>Acceptable</td><td>Acceptable</td><td>Acceptable</td></tr><tr><td colspan="2">$0.2 < \Phi \leq 0.5$</td><td>4</td><td>5</td><td>6</td></tr><tr><td colspan="2">$\Phi > 0.5$</td><td>0</td><td>0</td><td>0</td></tr></table> <p>5.4.4 Polarizer scratch</p> <p>(i) If the polarizer scratch can be seen after cover assembling or in the operating condition, judge by the linear defect of 5.3.</p> <p>(ii) If the polarizer scratch can be seen only in non-operating condition or some special angle, judge by the following:</p> <table><tr><th colspan="2" rowspan="2">Zone Size (mm)</th><th colspan="3">Acceptable Qty</th><th rowspan="2">C</th></tr><tr><th colspan="3">A+B</th></tr><tr><th>Length</th><th>Width</th><th>4.3''~7''</th><th>7~10.1''</th><th>>10.1''</th><th rowspan="4">Acceptable</th></tr><tr><td>Ignore</td><td>$W \leq 0.05$</td><td>Acceptable</td><td>Acceptable</td><td>Acceptable</td></tr><tr><td>$1.0 < L \leq 5.0$</td><td>$0.05 < W \leq 0.20$</td><td>4</td><td>5</td><td>6</td></tr><tr><td>$L > 5.0$</td><td>$W > 0.2$</td><td>0</td><td>0</td><td>0</td></tr></table>	Zone Size(mm)		Acceptable Qty			C	A+B					4.3''~7''	7~10.1''	>10.1''	Acceptable	$\Phi \leq 0.2$		Acceptable	Acceptable	Acceptable	$0.2 < \Phi \leq 0.5$		4	5	6	$\Phi > 0.5$		0	0	0	Zone Size (mm)		Acceptable Qty			C	A+B			Length	Width	4.3''~7''	7~10.1''	>10.1''	Acceptable	Ignore	$W \leq 0.05$	Acceptable	Acceptable	Acceptable	$1.0 < L \leq 5.0$	$0.05 < W \leq 0.20$	4	5	6	$L > 5.0$	$W > 0.2$	0	0	0	Minor
Zone Size(mm)		Acceptable Qty			C																																																										
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$L > 5.0$	$W > 0.2$	0	0	0																																																											
5	MURA	Using 3% ND filter, it's NG if it can be seen in R,G,B picture.																																																													
	White/Black dot (MURA)	Visible under: ND3%; $D \leq 0.15\text{mm}$, Acceptable; $0.15\text{mm} < D \leq 0.5\text{mm}$, $N \leq 4$; $D > 0.5\text{mm}$, Not allowable.			Minor																																																										

6	Glass defect	<p>(i) Crack</p> <p>Cracks are not allowed.</p> 	Minor								
		<p>(ii) TFT chips on corner</p>  <table border="1"><thead><tr><th>X</th><th>Y</th><th>Z</th><th>Acceptable</th></tr></thead><tbody><tr><td>≤ 3.0</td><td>≤ 3.0</td><td>Not more than the thickness of glass</td><td>$N \leq 3$</td></tr></tbody></table> <p>Chips on the corner of terminal shall not be allowed to extend into the ITO pad or expose perimeter seal.</p>	X	Y	Z	Acceptable	≤ 3.0	≤ 3.0	Not more than the thickness of glass	$N \leq 3$	Minor
		X	Y	Z	Acceptable						
≤ 3.0	≤ 3.0	Not more than the thickness of glass	$N \leq 3$								
<p>(iii) Usual surface crack</p>  <table border="1"><thead><tr><th>X</th><th>Y</th><th>Z</th><th>Acceptable</th></tr></thead><tbody><tr><td>≤ 1.5</td><td>≤ 1.5</td><td>Not more than the thickness of glass</td><td>$N \leq 4$</td></tr></tbody></table> <p>It is only applicable to the upper glass of LCD.</p>	X	Y	Z	Acceptable	≤ 1.5	≤ 1.5	Not more than the thickness of glass	$N \leq 4$	Minor		
X	Y	Z	Acceptable								
≤ 1.5	≤ 1.5	Not more than the thickness of glass	$N \leq 4$								

11.6 Module Cosmetic Criteria

Item No.	Items to be inspected	Inspection Standard	Classification of defects
1	Difference in Spec.	Not allowable	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
3	Soldering defects	No soldering missing	Major
		No soldering bridge	Major
		No cold soldering	Minor
4	Resist flaw on PCB	Visible copper foil ($\Phi 0.5$ mm or more) on substrate pattern is not allowed	Minor
5	FPC gold finger	No dirt, breaking, oxidation lead to black	Major
6	Backlight plastic frame	No deformation, crack, breaking, backlight positioning column breaking, obvious nick.	Minor
7	Marking printing effect	No dark marking, incomplete, deformation lead to unable to judge	Minor
8	Accretion of metallic Foreign matter	No accretion of metallic foreign matter (Not exceed $\Phi 0.2$ mm)	Minor
9	Stain	No stain to spoil cosmetic badly	Minor
10	Plate discoloring	No plate fading, rusting and discoloring	Minor
11	1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.	Minor
		b. Components side(In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor
	2. Flat packages	Either 'Toe'(A) or 'Seal'(B) of the lead to be covered by "Filet". Lead form to be assume over Solder. 	Minor
	3. Chips	$(3/2) H \geq h \geq (1/2) H$ 	Minor
	4. Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \geq 0.13$ mm. The diameter of solder ball $d \leq 0.15$ mm.	Minor
		b. The quantity of solder balls or solder splashes isn't beyond 5 in 600 mm ² .	Minor
		c. Solder balls/Solder splashes do not violate minimum electrical clearance.	Major