

WKS101WX002-WCT

PRODUCT: TFT TOUCH MODULE

MODULE NO. : WKS101WX002-WCT

SUPPLIER: WKS Technology Co.,LTD

DATE: Jun 28, 2018

SPECIFICATION

Revision: 0.1

WKS101WX002-WCT

This module uses ROHS material

This specification may change without prior notice in order to improve performance or quality. Please contact WKS R&D department for updated specification and product status before design for this product or release of this order.

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WKS101WX002-WCT

REVISION RECORD

REV NO.	REV DATE	CONTENTS	REMARKS
0.0	2017-05-31	First release	Preliminary
0.1	2018-06-29	Update some parameter for the consumption of	
		the LCD.	

CONTENTS

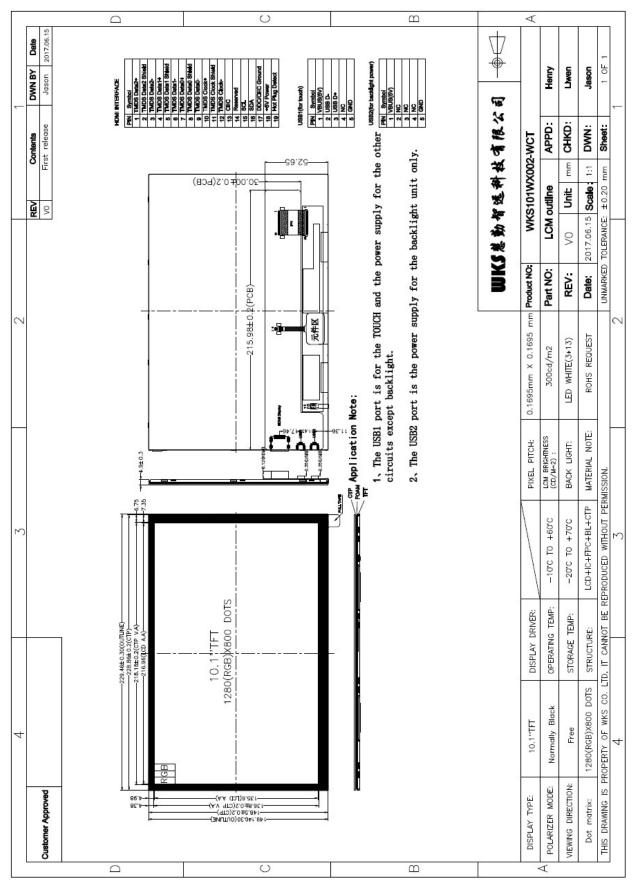
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1, GENERAL INFORMATION

Item of general information		Contents		
LCD Display Size (Diagonal)		inch		
Module Structure	LCD Displa	LCD Display + CTP Touch + PCB		
LCD Display Type	TFT/	TRANSMISSIVE	-	
LCD Display Mode	Ne	ormally Black	-	
Recommended Viewing Direction		Free	-	
Module size ($W \times H \times T$)	229.4	46×149.10×4.90	mm	
Active area (W×H)	21	mm		
Number of pixels (Resolution)	12	280RGB×800	pixel	
Pixel pitch (W×H)	0.	1695×0.1695	mm	
Color Pixel Arrangement		RGB Stripe	-	
	LCD Display	HDMI interface	-	
Module Interface Type	CTP I2C	USB interface	-	
G (Win7/Win8	/Win10(Plug and play)	-	
System Support	Android/Linux (need to be configured)		-	
Power Supply	USB1(400	-		
Color Numbers		-		
Backlight Type		White LED	-	

2, EXTERNAL DIMENSIONS





3, ABSOLUTE MAXIMUM RATINGS

Parameter of absolute maximum ratings	Symbol	Min	Max	Unit
Operating temperature	Тор	-10	60	$^{\circ}\!C$
Storage temperature	Tst	-20	70	$^{\circ}\!C$
Humidity	RH	-	90%(Max 60°C)	RH

Note: Absolute maximum ratings means the product can withstand short-term, not more than 120 hours. If the product is a long time to withstand these conditions, the life time would be shorter.

4, ELECTRICAL CHARACTERISTICS(DC CHARACTERISTICS)

Parameter of DC characteristics	Symbol	Min.	Тур.	Max.	Unit
Power supply Voltage	USB1	-	5.0	-	V
Backlight Power	USB2	-	5.0	-	V
Power supply Current	USB1	-	390	-	mA
	USB2	-	430	-	mA



5, CTP CHARACTERISTICS

Item of CTP characteristics	Specification	Unit	Remark
Panel Type	Glass Cover + Glass Sensor	-	-
Resolution	1280×800	pixel	-
Surface Hardness	6H	-	-
Transparency	≥86%	-	-
Interface Type	USB interface	-	-
Support Points	10(Max)	-	-
Sampling Rate	20~100	Hz	-
Supply voltage	3.3	V	-

6, ELECTRO-OPTICAL CHARACTERISTICS

Item o electro-op character	otical	Symbol	Condition	Min.	Typ.	Max.	Unit	Remark	Note
Response	time	Tr+Tf	0.0	-	25	50	ms	<i>FIG 1</i> .	4
Contrast I	Ratio	CR	$ heta{=}0 \\ arnote{=}0$	-	600	-	-	<i>FIG 2</i> .	1
Luminance un	Luminance uniformity		$Ta=25^{\circ}C$	-	80	-	%	<i>FIG 2</i> .	3
Surface Lum	inance	Lv	10 20 0	-	300	-	cd/m2	<i>FIG 2</i> .	2
CIE (x, y)		White x	$\theta = 0$	0.27	0.31	0.35			
chromaticity	White	White y	$\emptyset=0$ $Ta=25^{\circ}C$	0.28	0.32	0.36	-	<i>FIG 2</i> .	5
	Ø=90(1	2 o'clock)		75	85	-	deg		
Viewing	iewing Ø=270	(6 o'clock)	CD > 10	75	85	-	deg		
angle range	Ø=0(3	o'clock)	$CR \ge 10$	75	85	-	deg	<i>FIG 3</i> .	6
Ø=18		(9 o'clock)		75	85	-	deg		
NTSC ratio		-	-	-	50	-	%	-	-

Note 1. Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.:

Contrast Ratio(CR) = $\frac{\text{Average Surface Luminance with all white pixels(P1, P2, P3, P4, P5, P6, P7, P8, P9)}{\text{Average Surface Luminance with all black pixels(P1, P2, P3, P4, P5, P6, P7, P8, P9)}$

Note 2. Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv=Average Surface Luminance with all white pixels (P1,P2,P3,P4,P5,P6,P7,P8,P9)

Note 3. The uniformity in surface luminance (δ WHITE) is determined by measuring

luminance at each test position 1 through 9, and then dividing the maximum luminance of

9points luminance by minimum luminance of 9 points luminance. For more information see

FIG 2.

δWHITE= $\frac{Minimum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}{Maximum Surface Luminance with all white pixels (P1, P2, P3, P4, P5, P6, P7, P8, P9)}$

Note 4. The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%. For more information see FIG 1.

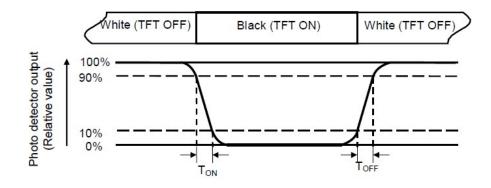
Note 5. CIE (*x*, *y*) *chromaticity*, *The x,y value is determined by screen active area position 5. For more information see FIG 2.*

Note 6. Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of contrast ratio is 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

Note 7. For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

Note 8. For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

FIG.1. The definition of Response Time



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FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance uniformity,

CIE (x, y) chromaticity

A : H/6 ; B : V/6 ;

H,V : *Active Area(AA) size*

Measurement instrument: BM-7; Light spot size=5mm, 350mm distance from the LCD surface to detector lens.

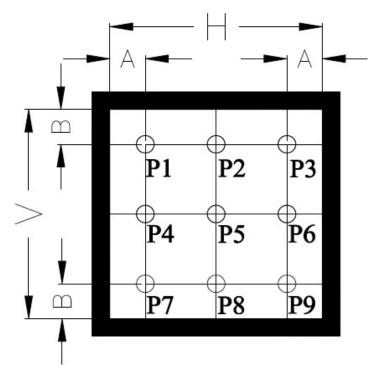
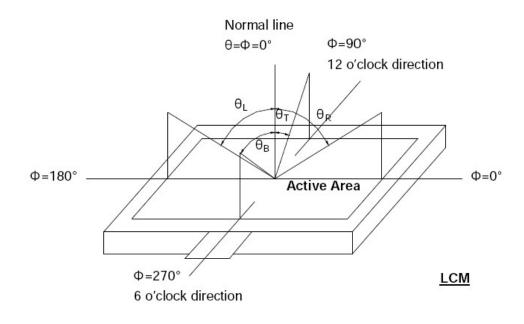


FIG.3. The definition of viewing angle





7, INTERFACE DESCRIPTION

A . **HDMI** Interface Description

<i>NO</i> .	Symbol	DESCRIPTION
1	TMDS Data2+	Positive side of channel 2 TMDS low-voltage signal
1		differential input pair
2	TMDS Data2 Shield	Ground
3	TMDS Data2-	Negative side of channel 2 TMDS low-voltage signal
5		differential input pair
4	TMDS Data1+	Positive side of channel 1 TMDS low-voltage signal
7		differential input pair
5	TMDS Data1 Shield	Ground
6	TMDS Data1-	Negative side of channel 1 TMDS low-voltage signal
0		differential input pair
7	TMDS Data0+	Positive side of channel 0 TMDS low-voltage signal
/		differential input pair
8	TMDS Data0 Shield	Ground
9	TMDS Data0-	Negative side of channel 0 TMDS low-voltage signal
,	TMDS Datao-	differential input pair
10	TMDS Clock+	Positive side of reference clock. TMDS low-voltage signal
10		differential input pair
11	TMDS Clock Shield	Ground
12	TMDS Clock-	Negative side of reference clock. TMDS low-voltage signal
12	TWID'S Clock-	differential input pair
13	CEC	No Connection
14	Reserved(N.C.)	No Connection
15	SCL	DDC SCL
16	SDA	DDC SDA
17	DDC/CEC Ground	Ground
18	+5V Power	+5V Power
19	Hot Plug Detect	Hot Plug Detect

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B, **USB1** Interface Description

<i>NO</i> .	Symbol	DESCRIPTION		
1	VUSB	USB Power(5V)		
2	D-	USB Data-		
3	D+	USB Data+		
4	NC	No connection		
5	GND	Power Ground		

C, USB2 Interface Description

<i>NO</i> .	Symbol	DESCRIPTION
1	VUSB	USB Power(5V) for Backlight Power
2	NC	No connection
3	NC	No connection
4	NC	No connection
5	GND	Power Ground

8, LCD TIMING

LCD Timing Table

Danamatan	Sure h al		T Tenid		
Parameter	Symbol	Min.	Тур.	Max.	- Unit
DCLK frequency@ Frame rate=60Hz	DCLK	68.9	71.1	73.4	MHz
Horizontal display area	thd	1280			DCLK
1 Horizontal Line	th	1340	1440	1470	DCLK
HSYNC pulse width	thpw	-	10	-	DCLK
HSYNC Back Porch(Blanking)	thb	-	80	-	DCLK
HSYNC Front Porch	thfp	-	70	-	DCLK
Vertical display area	tvd	800		Н	
VSYNC period time	tv	815	823	833	Н
VSYNC pulse width	tvpw	-	3	-	Н
VSYNC Back Porch(Blanking)	tvb	-	10	-	Н
VSYNC Front Porch	tvfp	-	10	-	Н



9, RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition
1	High Temperature Storage	70°C/120 hours
2	Low Temperature Storage	-20°C/120 hours
3	High Temperature Operating	60°C/120 hours
4	Low Temperature Operating	-10°C/120 hours
5	Temperature Cycle Storage	-10°C(30min.)~25(5min.)~60°C(30min.)×10cycles

A. Inspection after test:

Inspection after 2~4 hours storage at room temperature, the sample shall be free from

defects:

- *▶* Air bubble in the LCD;
- ➢ Sealleak;
- > Non-display;
- Missing segments;
- Glass crack;
- Current is twice higher than initial value.

B, Remark:

- > The test samples should be applied to only one test item.
- Sample size for each test item is 5~10pcs.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.



10, INSPECTION CRITERION

This specification is made to be used as the standard of acceptance/rejection criteria for TFT-LCD/IPS TFT-LCD module product, and this specification is applicable only in the case that the size of module equal to or exceed than 4.3 inch.

10.1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC

Z1.4-1993, normal level 2 and based on:

Major defect: AQL 0.65

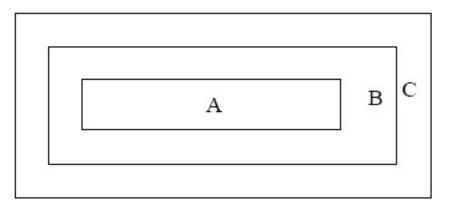
Minor defect: AQL 1.5

10.2 Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of 20~40W light intensity, all directions for inspecting the sample should be within 45 °against perpendicular line. (Normal temperature 20~25 °C and normal humidity 60 $\pm 15\%$ RH)

10.3 Definition of Inspection Item.

A, *Definition of inspection zone in LCD.*



Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

B, Definition of some visual defect

	Because of losing all or part function, bad pixel dots appear bright and the					
Bright dot	size is more than 50% of one dot in which LCD panel is displaying under					
	black pattern.					
Dark dot	Dots appear dark and unchanged in size in which LCD panel is displaying					
	under pure red, green, blue picture, or pure whiter picture.					

10.4 Major Defect

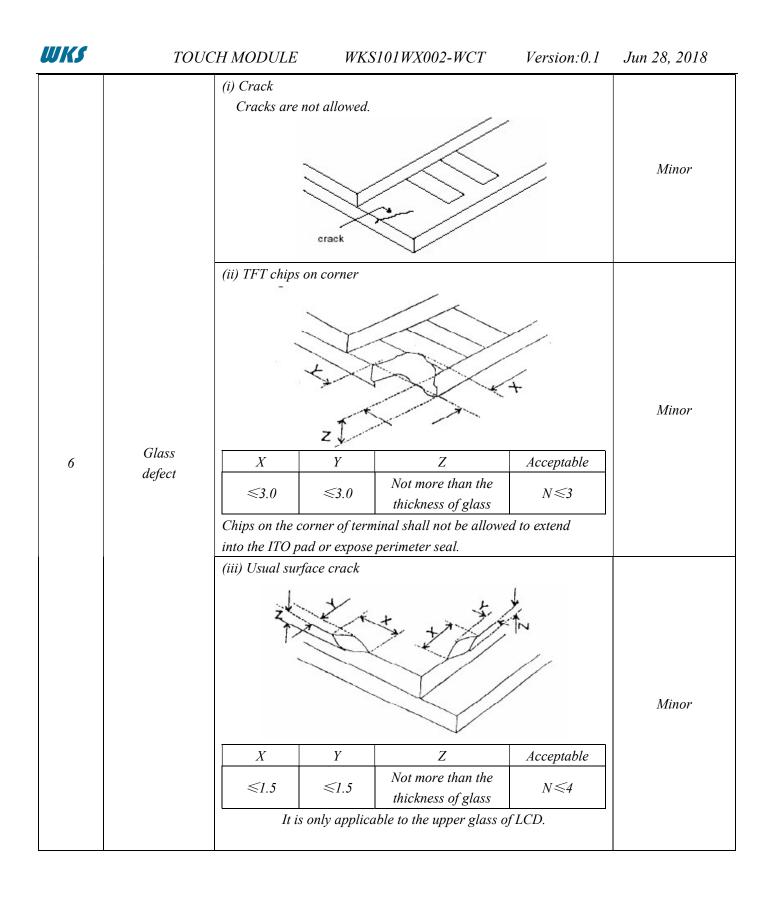
Item No.	Items to be inspected	Inspection standard	Classification of defects
1	Functional defects	 No display Display abnormally Missing vertical, horizontal segment Short circuit Excess power consumption Backlight no lighting, flickering and abnormal lighting 	major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	



10.5 Minor Defect

Item	Items to be	Inspection standard						Classification			
No.	inspected	*							of defects		
			Zone			Accept	able Qty				
						A+B					
					4.3" ∼ 7"	7~10.1	' ≥10.1"	С			
		Bright pixel dot		ot in the second s	1	2	3				
	Bright dot	D	ark pixel doi	t	4	4	4	A_{0}			
1	/dark dot	2brigh	acent	0	0	0	ccep	Minor			
	defect	2dark dots adjac		cent	0	0	0	Acceptable			
		Total i	bright and	dark	5	6	7	le			
			dots								
		Note: Min	imum dist	ance betw	veen defe	ctive dots	is more than .	5mm;			
		Pixel dots	' function	is normal	, but brig	ght dots ca	used by foreig	gn			
		material a	and other r	easons ar	re judged	l by the do	t defect of 5.2				
			Zone		Acceptable Qty						
		Size(mm) $\Phi \leq 0.2$		A+B							
	Dot defect figure x $\Phi = (x+y)/2$			4.3"~2	7" 7	~10.1"	>10.1"	С			
				Acceptable Acc		ceptable Acc	Acceptable	${\cal A}$			
		()îy	() î y	0.2 < 0	<i>⊅ <</i> 05				6	ccel	
2		$0.2 < \Phi \le 0.5$		4		5	6	Acceptable	Minor		
		Φ	>0.5	0		0	0	le			
		Note:									
		1. Minimi	ım distance	e between	defectiv	e dots is n	nore than 5 m	n;			
			antity of de		-						
				0	1	U					
			Zone		A	cceptable	Otv				
3	Linear defect										
			Size (mm)			A+B					
		Length	Width	4.3"~2	7" 7	~10.1"	>10.1"	С	Minor		
		defect Ignore L≤5.0	defect	Ignore	W≤0.05	Accepta	ible Ac	ceptable	Acceptable	Ac	mmor
			1<50	$\leqslant 5.0 \begin{array}{c} 0.05 < \\ W \leq 0.1 \end{array}$	л		F	6	Acceptable		
			$L \leq 3.0$		4		5	6	tabl		
		L>5.0	W>0.1	0		0	0	e'			

	1000	H MODU		WASIUIW.	X002-WCI	versio	n. 0.1	Jun 28, 2018
4	Polarizer defect	(i) Shiftin dimension (ii) Incon allowed. 5.4.2 Dirt Dirt whice 5.4.3 Pold Size(mm, Φ 0.2 < 0.2 < 5.4.4 Pol (i) If the portion the (ii) If the portion the	n. mplete cover to n polarize h can be we arizer Dente Zone $0 \le 0.2$ $\Phi \le 0.5$ 0.5 $0 \ge 0.5$ $0 \ge 0.5$ 0	on should not ering of the vi eer iped easily sho t & Air bubble 4.3"~7" Acceptable 4 0 eatch scratch can b condition, ju	exceed the gla ewing area du ould be accept Acceptable A+B $7\sim10.1"$ Acceptable 5 0 be seen after udge by the l be seen only e, judge by th Acceptable A+B $7\sim10.1"$ Acceptable 5 0	te to shifting is table. Qty >10.1" Acceptable 6 0 cover assemu inear defect of in non-oper ne following:	C Acceptable bling of 5.3. ating	Minor
5	MURA	Using	3% ND fili	ter, it's NG if i	it can be seen	in R,G,B picti	ure.	Minor
	White/Black dot (MURA)	V 0.1.						





10.6 Module Cosmetic Criteria

Item No.	Items to be inspected	Inspection Standard	Classification of defects	
1	Difference in Spec.	Not allowable	Major	
2	Pattern peeling	No substrate pattern peeling and floating	Major	
3		No soldering missing	Major	
	Soldering defects	No soldering bridge	Major	
		No cold soldering	Minor	
4	Resist flaw on PCB	Visible copper foil ($\Phi 0.5$ mm or more) on substrate pattern is not allowed	Minor	
5	FPC gold finger	No dirt, breaking, oxidation lead to black	Major	
6	Backlight plastic frame	No deformation, crack, breaking, backlight positioning column breaking, obvious nick.	Minor	
7	Marking printing effect	No dark marking, incomplete, deformation lead to unable to judge	Minor	
8	Accretion of metallic Foreign matter	No accretion of metallic foreign matter (Not exceed Φ 0.2mm)	Minor	
9	Stain	No stain to spoil cosmetic badly	Minor	
10	Plate discoloring	No plate fading, rusting and discoloring	Minor	
	1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.	Minor	
		b. Components side(In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor	
11	2. Flat packages	Either 'Toe'(A) or 'Seal'(B)of the lead to be covered by "Filet". Lead form to be assume over Solder.	Minor	
11	3. Chips	$(3/2) H \ge h \ge (1/2) H$	Minor	
	4. Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad $h \ge 0.13$ mm. The diameter of solder ball $d \le 0.15$ mm.	Minor	
		b. The quantity of solder balls or solder splashes isn't beyond 5 in 600 mm2.	Minor	
		c. Solder balls/Solder splashes do not violate minimum electrical clearance.	Major	



11, PRODUCT PHOTOS

PRODUCT



APPLICATION CASE

Driven by Raspberry Pi (linux) & win7/win8/win10 systems

