DMG48320T035_15WTR

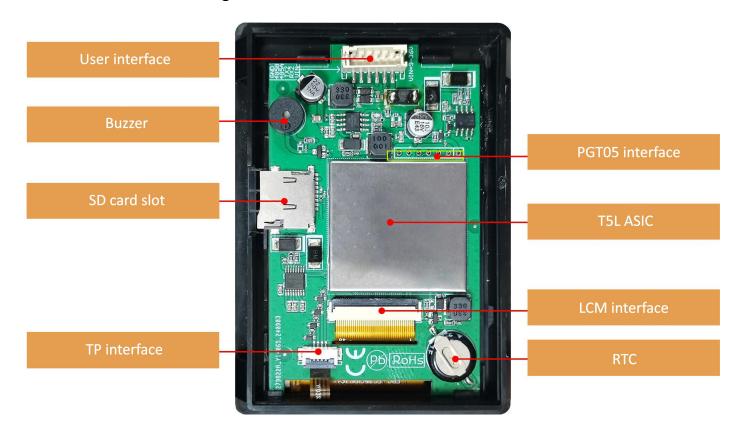
Features:

- Powered by T5L0 ASIC, running DGUS II HMI platform, industrial-grade smart LCM.
- 3.5 inch, 320*480 resolution, IPS-TFT LCD.
- Reliable resistive touch panel.
- With enclosure.



1. Hardware and interface

1.1 Hardware interface diagram



Hardware interface diagram

1.2 Hardware and interface description

No.	Item	Description
1	T5L0 ASIC	DWIN independently developed, mass production in 2020. Dual 8051 cores, GUI and application run on separate 8051 cores.
2	User interface	6Pin_2.0mm socket for power supply and serial communication.
3	Flash	16MBytes (1*16MBytes NOR Flash) for storing UI files like fonts, images, music, with over 100,000 erase/write cycles.
4	Buzzer	3V passive buzzer.
5	RTC	Super-capacitor powered, accuracy: ±20ppm @25°C, maintains operation for 7 days after power-off.
6	SD card slot	For DGUS project file downloads (UI, CFG files, kernel, etc.), 4 Mb/s rate.
7	PGT05 interface	For programming DGUS firmware.

2. Specification parameters

2.1 Display parameters

LCD Type	IPS, TFT LCD.
Viewing Angle	Wide viewing angle (85°/85°/85°/85° typical), high contrast, and good color reproduction.
Resolution	320×480 (support 0°/90°/180°/270°)
Active Area (AA)	49.00mm (W) ×73.40mm (H)
Backlight	LED
Backlight Service Life	>30000 hours
Brightness	200nit
Brightness Control	100-level brightness adjustment (Flickering may occur at 1%-30% of max brightness; not recommended for use in this range)

Note: Use dynamic screen saver to prevent afterimages from prolonged fixed page display.

2.2 Touch parameters

Туре	Resistive touch panel.
Structure	ITO film + ITO glass structure and hardness 3H.
Light Transmittance	78%
Life	Over 1,000,000 times touch.

2.3 Serial interface parameters

Mode	UART2: RS232 UART4: RS485 (Only	available after OS	S configuration)				
	Test Condition	Min	Тур	Max	Unit			
Voltage Level (RXD, TXD)	Output 1	-	-5.0	-3.0	V			
	Output 0	3.0	5.0	-	V			
	Input 1	-15.0	-5.0	-	V			
	Input 0	-	5.0	15.0	V			
Baud Rate	3150~3225600bps, ty	3150~3225600bps, typical value of 115200bps.						
	Test Condition	Min	Тур	Max	Unit			
	Output 1	2.5	5.0	-	V			
Voltage Level (V_AB)	Output 0	-	-5.0	-2.5	V			
(,	Input 1	0	2.5	-	V			
	Input 0	-	-2.5	-0.2	V			
Baud Rate	3150~921600bps, typ	ical value of 1152	00bps.					
Data Format	UART2: N81 UART4: N81/E81/O8 ²	UART2: N81 UART4: N81/E81/O81/N82 ,4 modes (OS configuration)						
Interface Cable	6Pin_2.0mm							

2.4 Electrical specifications

Rated Power	<5W			
Operating Voltage	9-36V, typical val	ue of 12V.		
Operating Current	78mA	VCC=12V, max backlight.		
Recommended power supply: 12V 1A DC.				

2.5 Operating environment

Operating Temperature	-20℃ to 70℃ (12V @ 60% RH)
Storage Temperature	-30℃ to 80℃
Anti-UV	None
Conformal Coating	Yes
Operating Humidity	10%-90%RH, typical value of 60% RH.

3. Reliability test

3.1 Electrostatic discharge test

Test temperature: 25°C. Test humidity: 50%RH.

Test process: Place the product on the test bench fixture (approximately 15cm in height), and perform contact and air discharge tests on the smart LCM. Observe if any freezing, black or white screen, flickering, or rebooting occurs during the test.

Test conclusion: The product's ESD performance meets GB/T 17626.2 Class B standards.

■ Test standard : □EN 61000-4-2:2009	☐ IEC 61000-4-2:2008	□GB/T 17626.2-2018
□Other:		

Table 1: Electrostatic Discharge Immunity (Air Discharge)

Tost Daints Laustians				Test Le	evels			
Test Points Locations	-2kV	+2kV	-4kV	+4kV	-8kV	+8kV	-15kV	+15kV
Screen					A	0		
1	1	1	1	1	1	1		1
1	1	1		1	1	1	1	1
1	1	1	1	/	1	1	/	1

Table 2: Electrostatic Discharge Immunity (Direct Contact)

Tost Doints Locations	Test Levels							
Test Points Locations	-2kV	+2kV	-4kV	+4kV	-6kV	+6kV	-8kV	+8kV
Jo		(/	/		/		/
1	1	1	1	1	1	1	1	1
1	1	1	1	1	1	1	1	1
1	/	1	1	1	1	1	1	1

3.2 EFT test

Test temperature: 25°C. Test humidity: 50%RH.

Test process: Place the product flat on the test bench, power the smart LCM through the power supply coupled with an impulse generator. Observe if any reboot, abnormal display, or touch malfunction occurs during the test.

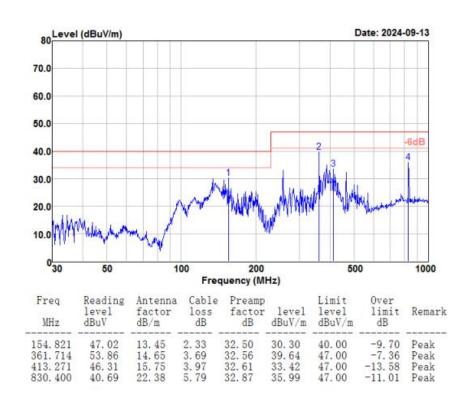
Test conclusion: The product's EFT performance meets GB/T 17626.4 Class B standards.

	□Other:								
52-155.02					Test Lev	vels(kV)			
Test F	oints	-0.5	+0.5	-1.0	+1.0	-2.0	+2.0	-4.0	+4.0
	L					19	19		
	N					19	19		
-DY-	Earth								
Power ports	L+N					13	19		1
	L + Earth						- 17-380		
	N + Earth	<u> </u>							
	L+N+Earth								
Signal ports									

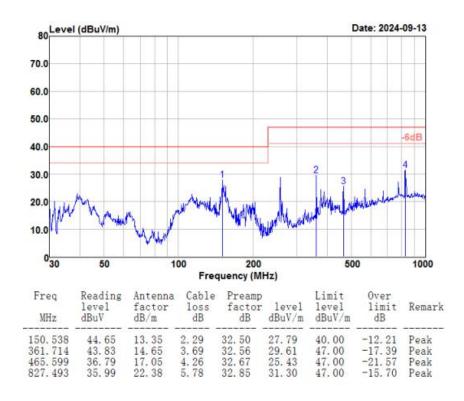
3.3 RE test

Test Item	Test Standard	Result		
RE	Class B	Normal operation		

HORIZONTAL



VERTICAL



3.4 CS test

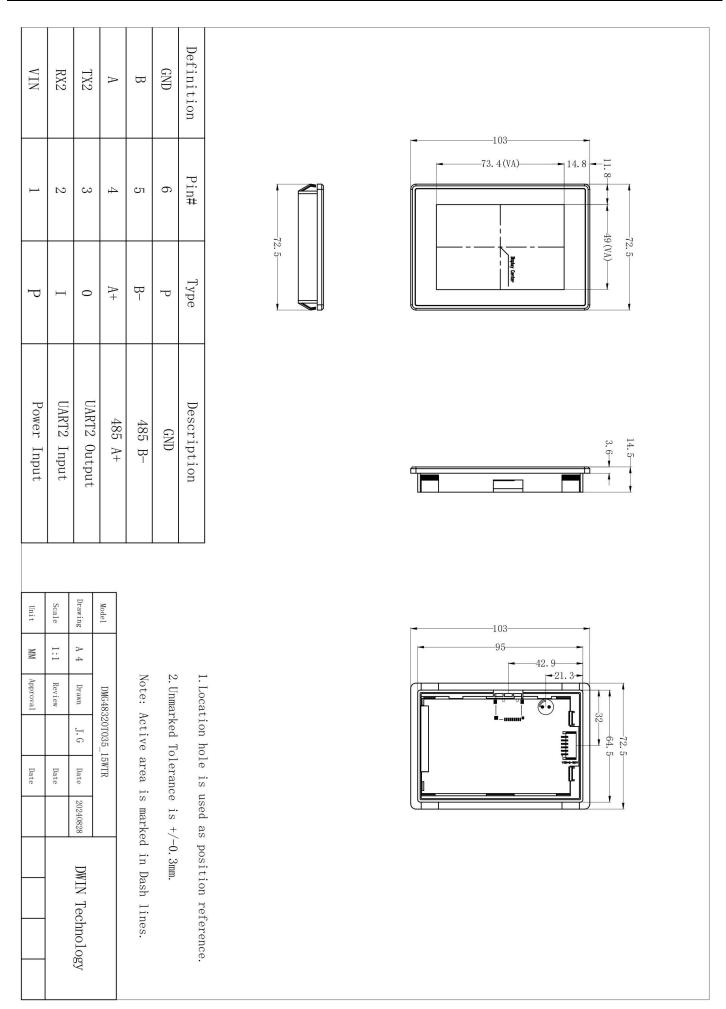
■ Test standard : □EN		□IEC 61000-4-6:2013	□GB/T 17626.6-2	017
■Modulation: ☑Ampl	itude 80%,1kHz sin	e wave	0%,2Hz sine wave	□Other:
Dwell time: ☑1s Frequency Step Size :	□3s □other: ☑1 % of preceding f	frequency value othe	er:	
Coupling Line	Frequency Rar (MHz)	nge Voltage	Level(e.m.f.) (V)	Result

4. Packaging & dimensions

Form Factor	72.50mm (W)×103.00mm (H)×14.50mm (T)
Net Weight	84g

Packaging Standards

Model	Dimensions Layer Quantity/Layer		Quantity(Pcs)	
Carton1:	220mm(L)×160mm(W)×47mm (H)	1	2	2
Carton2:	250mm(L)×200mm(W)×80mm (H)	2	2	4
Carton3:	320mm(L)×270mm(W)×80mm (H)	2	4	8
Carton4:	525mm(L)×330mm(W)×80mm (H)			-
Carton5:	570mm(L)×340mm(W)×385mm (H)	2	60	120



5. T5L series IC features

- (1) Mature and stable 8051 core which is the most widely used with the maximum operating frequency of T5L is up to 250MHz, 1T(single instruction cycle)high speed operation.
- (2) Separate GUI CPU Core running DGUS II System:
 - High-speed display memory, 2.4GB/S bandwidth.
 - 2D hardware acceleration, the decompression speed of JPEG is up to 200fps@1280*800 and the UI with animation and icons as its main feature is extremely cool and smooth.
 - Images and icons stored in JPEG format. Adopt Low-cost 16Mbytes SPI Flash.
 - Support CTP or RTP with adjustable sensitivity and maximum 400 Hz touch frequency.
 - 1-way 15bit 32Ksps PWM digital power amplifier driver loudspeaker, save power amplifier cost and achieve high signal-to-noise ratio and sound quality restoration.
 - 128Kbytes variable storage space for exchanging data with OS CPU Core and memory.
 - Support DGUS development and simulation on PC. Support background remote upgrade.
- (3) Separate CPU (OS CPU) core runs user 8051 code or DWIN OS system and user CPU is omitted in practical application:
 - Standard 8051 architecture and instruction set, 64Kbytes code space, 32Kbytes on-chip RAM.
 - 64 bit integer mathematical operation unit (MDU), including 64 bit MAC and 64 bit divider.
 - 28 IOs, 4-channel UARTs, 1-channel CAN, up to 8-channel 12-bit A/Ds and 2-channel 16-bit PWM of adjustable resolution.
 - Support IAP on-line simulation and debugging with unlimited number of breakpoints.
 - Upgrade code online through DGUS system.
- (4) 1Mbytes on-chip Flash with DWIN patent encryption technology ensure code and data security.
- (5) Operating temperature ranges from -40°C to +85°C(IC operating temperature customizable from -55°C to 105°C).

DWIN encourages users to design your own customized product based on T5L

6. Revision records

Rev	Revise Date	Content	Editor
00	2024-11-11	First Edition	Xu Ying

Please contact us if you have any questions about the use of this document or our products, or if you would like to know the latest information about our products:

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Thank you all for continuous support of DWIN, and your approval is the driving force of our progress!

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